

sub B1
1. (Amended) A burst signal detection circuit comprising:

a DC variation removing circuit detecting the bottom level or the peak level of an input signal and removing the DC level variation of the input signal by differentially amplifying the difference between the input signal level and the peak level or bottom level of the input signal; and

an amplitude identifying circuit detecting the presence or absence of a burst signal in said input signal based on the output signal from the DC variation removing circuit;

said amplifying identifying circuit including:

an amplitude detection circuit detecting the maximum amplitude of the output signal of said DC variation removing circuit;

a threshold level control circuit controlling a threshold; and

a comparator circuit comparing the output level of said amplitude detection circuit with said threshold level and outputting a detection signal indicating the presence or absence of the burst signal.

10. (Amended) A burst signal detection circuit comprising:


a DC variation removing circuit detecting the bottom level or the peak level of an input signal and removing the DC level variation of the input signal by differentially amplifying the difference between the input signal level and the peak level or bottom level of the input signal;

a signal amplifier amplifying the output signal of said DC variation removing circuit; and

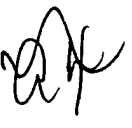
an amplitude identifying circuit detecting the presence or absence of a burst signal in said input signal based on the output signal from said signal amplifier;

said amplitude identifying circuit including:

- an amplitude detection circuit detecting the maximum amplitude of the output signal of said signal amplifier;
- a threshold level control circuit controlling the threshold level; and
- a comparator circuit comparing the output level of said amplitude detection circuit with said threshold level and outputting a detection signal indicating the presence or absence of the burst signal.

 19. (Amended) A burst signal detection circuit comprising an amplitude identifying circuit including:

- an amplitude detection circuit detecting the bottom level or the peak level of an input signal, removing the DC level variation of the input signal by differentially amplifying the difference between the input signal level and the peak level or bottom level of the input signal, and detecting the maximum amplitude of said input signal;
- a threshold level control circuit controlling a threshold level; and
- a comparator circuit comparing the output level of said amplitude detection circuit with said threshold level and outputting a detection signal indicating the presence or absence of the burst signal.

 28. (Amended) A burst signal detection circuit comprising:

- a DC variation removing circuit detecting the bottom level or the peak level of an input signal, and removing the DC level variation of the input signal by differentially amplifying the difference between the input signal level and the peak level or bottom level of the input signal, and for amplifying said input signal; and